

Howard College Semester 1 2025

Module timetable - ENEL4DS, ENEL4DS H1 (B1) (CE EC) Digital Signal Processing (Wks 7-13, 15-21 (2024 SEM 1), 2025/02/09 ... 2025/05/18)

	07:45am	08:40am	09:35am	10:30am	11:25am	12:20pm	01:15pm	02:10pm	03:05pm	04:00pm	04:45pm
Mo											
Tue											
Wednesday							Wks 7-13, 15, 17-21, 2025/02/12 ... 2025/05/21 Module: ENEL4DS (ENEL4DS H1 (B1) (CE EC) Digital Signal Processing) (2003) Room: S2 - 108- T Crestron (019 00 41) (SLTC Science Building)				

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Thursday				Wks 7-13, 15-17, 19-21, 2025/02/13 ... 2025/05/22 Module: ENEL4DS (ENEL4DS H1 (B1) (CE EC) Digital Signal Processing) (2003) Room: S2 - 108- T Crestron (019 00 41) (SLTC Science Building)							
Fri											