

HC SEMESTER 2 2020

Module timetable - ENEL4DS, ENEL4DS H1 (B1) (CE EC) Digital Signal Processing (Wks 39-51 (2020 SEM 2 Revised), 2020/09/20 - 2020/12/13)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM
Mo											
Tue											
We											
Thu											
Fri											