

HC SEMESTER 1 2020

Module timetable - ENEL4CA, ENEL4CA H1 (A2) (CE) Computer Engineering Design 3 (Wks 6-14, 16-20 (2020 SEM 1), 2020/02/02 ... 2020/05/10)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM	05:30PM
Monday								Wks 6-14, 17, 19-20, 2020/02/03 ... 2020/05/11 Module: ENEL4CA (ENEL4CA H1 (A2) (CE) Computer Engineering Design 3) Room: EE - G-02 - 44 F (001 00 G02) (Engineering North)				
Tuesday								Practical, 02:10PM-05:10PM, Wks 6-14, 17-20, 2020/02/04 ... 2020/05/12 Modules: ENEL4CA (ENEL4CA H1 (A2) (CE) Computer Engineering Design 3); ENEL4DA (ENEL4DA H1 (A2) (EC) Electronic Design) (2003); ENEL4HA (ENEL4HA H1 (F3) (EL) High Voltage Engineering) Room: LAB: ElecEng B-03s (001 00 G07) (Engineering South)				
We												
Thu												
Fri												