

HC SEMESTER 1 2020

Module timetable - ENEL3CA, ENEL3CA H1 (B1) (CE) Computer Engineering Design 1 (Wks 6-14, 16-20 (2020 SEM 1), 2020/02/02 ... 2020/05/10)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM	05:30PM
Mon								Practical, 02:10PM-05:10PM, Wks 6-14, 17, 19-20, 2020/02/03 ... 2020/05/11 Module: ENEL3CA (ENEL3CA H1 (B1) (CE) Computer Engineering Design 1) Room: LAB: ElecEng B-03s (001 00 G07) (Engineering South)				
Tue								Practical, 02:10PM-05:10PM, Wks 6-14, 17-20, 2020/02/04 ... 2020/05/12 Module: ENEL3CA (ENEL3CA H1 (B1) (CE) Computer Engineering Design 1) Room: LAB: ElecEng 3rd YEAR (001 01 1-13) (Engineering South)				
We												
Thursday				Wks 6-14, 16-19, 2020/02/06 ... 2020/05/07 Module: ENEL3CA (ENEL3CA H1 (B1) (CE) Computer Engineering Design 1) Room: S1 - 108 - T Crestron (019 00 46) (SLTC Science Building)								
Fri												