

HC SEMESTER 1 2020

Module timetable - ENEL4DS, ENEL4DS H1 (B1) (CE EC) Digital Signal Processing (Wks 6-14, 16-20 (2020 SEM 1), 2020/02/02 ... 2020/05/10)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM	05:30PM
Mo												
Tue												
Wednesday							Wks 6-14, 17-20, 2020/02/05 ... 2020/05/ 13 Module: ENEL4DS (ENEL4DS H1 (B1) (CE EC) Digital Signal Processing) (2003) Room: S2 - 108- T Crestron (019 00 41) (SLTC Science Building)					

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Thursday				Wks 6-14, 16-19, 2020/ 02/06 ... 2020/05/07 Module: ENEL4DS (ENEL4DS H1 (B1) (CE EC) Digital Signal Processing) (2003) Room: S2 - 108- T Crestron (019 00 41) (SLTC Science Building)								
Fri												