

Howard College 2018 Semester 1

Module timetable - ENEL4DS, ENEL4DS H1 (B1) (CE EC) Digital Signal Processing (Wks 6-12, 14-20 (2018 SEM 1), 2018/02/04 ... 2018/05/13)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM	05:30PM
Mo												
Tue												
Wednesday							Wks 6-11, 14-20, 2018/02/07 ... 2018/05/ 16 Module: ENEL4DS (ENEL4DS H1 (B1) (CE EC) Digital Signal Processing) (2003) Room: S2 - 108- T Crestron (019 00 41) (SLTC Science Building)					

Howard College 2018 Semester 1

Module timetable - ENEL4DS, ENEL4DS H1 (B1) (CE EC) Digital Signal Processing (Wks 6-12, 14-20 (2018 SEM 1), 2018/02/04 ... 2018/05/13)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM	05:30PM
Thursday				<div>Wks 6-8, 14-20, 2018/02/08 ... 2018/05/17</div> <div>Module: ENEL4DS (ENEL4DS H1 (B1) (CE EC) Digital Signal Processing) (2003) Room: S2 - 108- T Crestron (019 00 41) (SLTC Science Building)</div>								
Fri												