

2017HC

Module timetable - ENEL3DB, ENEL3DB H2 (B2) (EC) Electronic Design 2 (Wks 29-37, 39-43 (2017 SEMESTER TWO), 7/16/2017 ... 10/22/2017)

	07:45AM	08:40AM	09:35AM	10:30AM	11:25AM	12:20PM	01:15PM	02:10PM	03:05PM	04:00PM	04:45PM	05:30PM
Mo												
Tuesday								Wks 29-31, 33-37, 39-43, 7/18/2017 ... 10/24/2017 Module: ENEL3DB (ENEL3DB H2 (B2) (EC) Electronic Design 2) Room: CC 3 - 60 F (059 01 1-22S) (Engineering South)				
We												
Thu												
Fri												